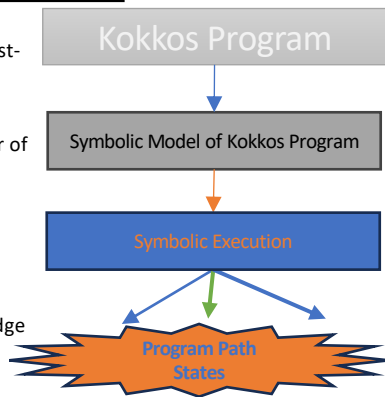


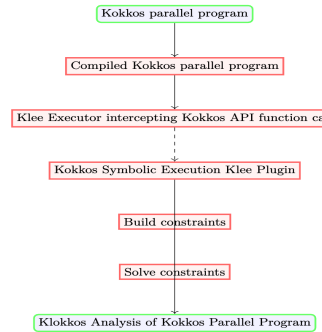
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Testing a Kokkos Parallel Program is Hard

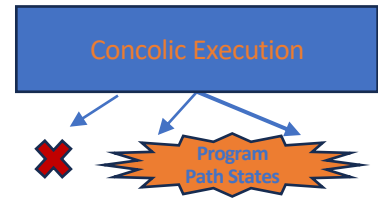
- Background:** Kokkos is single model sitting on top of host-device backend, e.g., OpenMP-CUDA
- Problem:** Once portability is introduced, more prone to bugs (number of bugs grows exponentially with number of combinations of backends) than bare OpenMP-CUDA
- Fix:** Symbolic execution to aid programmer to identify bugs
- Still a Problem:** program path state explosion
- Research Question:** prune state explosion with knowledge of scientific and parallel computing?



LLVM Klee's Concolic Execution of Kokkos Program

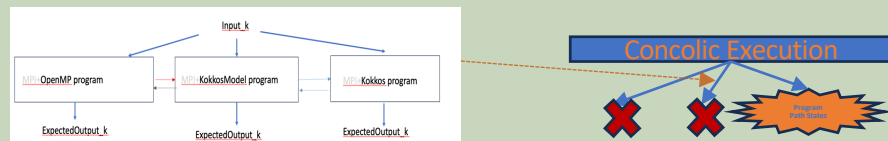


- Symbolic execution with common inputs for program --> LLVM Klee
- Challenge: Still very large number test cases in parallel program
- How do we further prune the number of cases?



Database of Parallel Programs → Prune Even More

- Identify examples based on use cases, computational patterns, kind of bug
- Classify or tag real applications associated with these
- Guided symbolic analysis queries mistake examples during symbolic execution to hone in on error in Kokkos program in question
 - Via LLVM clang AST transform
- Machine learning to develop rules for correctness for future analysis



Auto-Testing Science Simulations

- Use case: From Kokkos LAMMPS → Concolic Analysis: Consider the input of 1 million particles are distributed across 8 threads, distance at most 1 nn
- By providing heterogeneous bug in the stencil code, formal model can identify or specify rule for bug.
- Further guiding by examples: (1) check equivalence with buggy proxy stencil (2) If not equivalent, generate paths of proxy stencil given input and check for data race.
- Augment formal model to specify rule for bug.
- Store rules in database for future analysis

Future work: Automatic synthesis of Kokkos mistake examples via LLVM AST translation

References:

- K. Teranishi, S. Mukherjee, S. Pollard, R. Rutledge, A. Orso, V. Sarkar. *Towards Automated Test Synthesis of Performance Portable Programs*. Klee 2022 workshop, London, United Kingdom. September 2022.
- LLVM Project. <https://llvm.org>